

**786LCD/mITX Boards BIOS revision information****786lm001.rom**                      **11:22 19/07-05**  
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VIA raiser is supported and must be jumpered as device 6.  
Com 3&4 added.  
SATA rom added.  
AGP startup problem fixed.  
New setup engine, Options will now be listed if there are no help present.  
BBS module upgraded -> beta 2-8.00.11, if newer oprom will not be executed.  
80 Pin detection and setup added for IDE.  
Hide/disable of Lan1-3, FW & SATA added.  
Serial + Part number readout added.  
Vid readout added.  
Voltage & HW monitor running.  
Legacy USB currently only on port 0 & 1 (Upper bracket & outer header24-J6)  
USB CMOS index corruption

**786lm002.rom**                      **9:59 29/09-05**  
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Control of audio amp added.  
Secure CMOS added.  
IIH added.  
USB detection now runs in 4 slots except in J31 (in the corner near FW)  
Sys and CPU temp adjusted.  
133MHz DRAM setup added (optional bank B mounting allowed, bottom side)  
AGP clock disabled if not used.

Post codes regarding SDRAM detection :

## Error Code   Error Description

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E0h	DIMM is not SDRAM
E1h	Buffered or Registered SDRAM
E2h	SPD Revision not valid
E3h	133MHz memory is selected in CMOS but Non-133MHz DIMM are present.
E4h	No memory has been found
E6h	Invalid SPD data byte 31
E7h	Invalid SPD data byte 05
E8h	SMBus transaction error
E9h	Invalid SPD data byte 19
EAh	ECC DIMM present
EBh	Parity DIMM present
ECh	Invalid SPD data byte 11
EDh	Invalid SPD data byte 61
EEh	133 SM selected but 133 FSB not found
EFh	Memory R/W pattern test error

Remove redundant FWH resources from ACPI.  
Add display setup.  
Assigned APIC IRQ to VGA preventing crash during setup of win2K.

**786lm003.rom****10:42 14/11-05**  
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fix "movesi, dword ptr cs:rsdt\_ptr" vs. "cs:pACPITbls4End\_ptr"  
S3 working with correct clock on wake  
Power AC state added.  
wake kbd/mouse added.  
Invert power button GPBASE 52D = 20.  
Fix HWmon setup after S3  
Sflash disabled.  
Floppy setup removed.  
EHCI sub/vendor register setup now. (Fixing wake from S3 D29F7)  
COM4 IRDA option removed.  
CMOS corruption force based on bios minor ver.  
Fixed Win2k chipset drivers with COM ports  
Com C & D IRQ option will be hided when port is disabled.  
Force keyboard present when read from CMOS.

**786lm04.rom****10:59 1/3-05**  
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Fixed mouse double click wake in WinXP from S1 (Problem when entered standby with a double click, system wakes again).  
Add Thermal "stop clock" setup for CPU + External temperature.  
Always On/Off/Last power-on options supported.  
Changed Com port selection, each port can exclusively be: I/O=3F8 / IRQ=4, I/O=2F8 / IRQ=3, I/O=3E8 / IRQ=4, I/O=2E8 / IRQ=3, I/O=3E8 / IRQ=6, I/O=3E8 / IRQ=10 or I/O=2E8 / IRQ=11  
Com C & D default Disabled (To free more IRQs)  
Removed inactive BIOS setup items  
IRQ routing corrected for the VIA raiser.

**786lm05.rom****9:35 22/3-05**  
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Allow board variants without onboard DRAM

Fix Extended Memtest now starting @ 24MB, 8MB before... (If Quickboot was disabled VBIOS + Serial number + Display block was wiped)

Fix Primary Master + Primary Master harddisk type NOT installed issue with load failsafe using OEM defaults

Vendor code added for user defined key to default CMOS settings:

"BF epoke 8 1" will setup ESC key to be used.

"BF epoke 8 ff" will set back to normal.

Common keys :

01 ESC

0F TAB

1D left Ctrl

29 §/½

3A Capslock

3B-44 F1-F10

45 NumLock

If running with secure CMOS, the original failsafe values will be loaded.

If running with OEM defaults these values will be loaded

The key must be pressed no later than approx 1 sec. after power on.

Fixed CMOS jumper sticky issue

Correct strings for Remote BIOS Access.

Display block V0.02 added.

Added logo support

**786lm06.rom****15:07 3/4-06**  
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Fixed bug with FAN RPM target display when CS is out of alignment.

**786lm07.rom****10:07 05/7-06**  
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RTC alarm added

UDMA 6 fixed, will only run in UDMA 5 in DOS

Watchdog added (32 or 76 sec)

CPU speed is now displayed correctly in bios on the 400MHz board version

Memory speed selection is now shaded when a 400MHz CPU is found

**786lm08.rom**                      **09:02 05/9-06**  
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New display block added (V0.03)  
Quiet Boot option improved

**786lm09.rom**                      **09:17 14/11-06**  
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CPU Voltage displayed changed from 1,65V -> 1,15V  
Com port options added: 3E8h IRQ 9 and 2E8 IRQ 5

**786lm10.rom**                      **11:50 01/02-07**  
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Floppy support added  
When Floppy is enabled then the addr 0x378 is free

**786lm11.rom**                      **11:50 22/05-07**  
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WOL function added for ACPI S5; must be enabled in BIOS APM menu  
Wake from ACPI S3 fixed, incl. RTC & WOL

WOL function not supported from ACPI S1:

If WOL is disabled in setup a WOL package can wake XP from S1, resulting keyboard & mouse not working. If WOL is enabled in setup a WOL package will not wake from S1.

Added display block V0.06